

Applicant(s): Lee, et al.
U.S. Serial No.: 09/737,540

REMARKS

The amendments to the specification are made to clarify the description. No new matter is added to the application.

Attached hereto is a marked-up version of the changes made to the claims by the current Amendment. The attached pages are captioned "Version with Markings to Show Changes Made."

Respectfully submitted,


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Version with Markings to Show Changes Made

In the Specification

The paragraph at page 9 lines 2 through 4 has been amended as follows:

FIGs. 3A to [3G] 3F are schematic cross-sectional views which illustrate an embodiment of the method for manufacturing a wiring of a semiconductor device according to the present invention.

The paragraph at page 10 lines 8 through 12 has been amended as follows:

FIGs. 3A to [3G] 3F are schematic cross-sectional views which illustrate an embodiment of the method for manufacturing a wiring of a semiconductor device according to the present invention. The manufacturing method of a wiring of a semiconductor device illustrated in FIG. 2 will be explained in detail with reference to FIGs. 3A to 3F hereinbelow.

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